

REISSUE APPLICATION DECLARATION BY THE INVENTOR

Docket Number (Optional)

057454-0093

I hereby declare that:

Each inventor's residence, mailing address and citizenship are stated below next to their name.

I believe the inventors named below to be the original and first inventor(s) of the subject matter which is described and claimed

In patent number 5,898,606, granted April 27, 1999 and for which a

reissue patent is sought on the invention entitled ELECTRICALLY PROGRAMMABLE AND ERASABLE

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND OPERATING METHOD THEREFOR

the specification of which

☐ is attached hereto.

☒ was filed on April 27, 2001 as reissue application number 09/843,165

and was amended on April 27, 2001
(If applicable)

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

☒ I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b). Attached is form PTO/SB/02B (or equivalent) listing the foreign applications.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

☐ by reason of a defective specification or drawing.

☒ by reason of the patentee claiming more or less than he had the right to claim in the patent.

☒ by reason of other errors.

At least one error upon which reissue is based is described below. If the reissue is a broadening reissue, such must be stated with an explanation as to the nature of the broadening:

It is not clear in the patent claims whether or not the main bit line and the subbit lines are formed in the same layer on the substrate. It is difficult to form all the main bit lines and subbit lines in the same layer. The main bit line and subbit lines can only be formed in the same layer on the substrate in a limited region of the device.

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 2)

Docket Number (Optional)

057454-0093

All errors corrected in this reissue application up to the time of filing the oath or declaration arose without any deceptive intention on the part of the applicant.

Note: To appoint a power of attorney, use form PTO/SB/81.

Correspondence Address: Direct all communications about the application to:

☒ The address associated with Customer Number:
OR

20277

☐ Firm or
Individual Name

Address

City

State

Zip

Country

Telephone

Email

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

Full name of sole or first inventor (given name, family name) Shinichi KOBAYASHI

Inventor's signature

Shinichi Kobayashi

Date

June 16, 2008

Residence Kumamoto-shi, Kumamoto, JAPAN

Citizenship Japanese

Mailing Address Room No. 201, Sato Apato, 2-25, Shinyashiki 3-chome, Kumamoto-shi, Kumamoto 862-0975 JAPAN

Full name of sole or first inventor (given name, family name) Yasushi TERADA

Inventor's signature

Yasushi Terada

Date

June 16, 2008

Residence Chiyoda-ku, Tokyo, JAPAN

Citizenship Japanese

Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN

Full name of sole or first inventor (given name, family name) Yoshikazu MIYAWAKI

Inventor's signature

Yoshikazu Miyawaki

Date

June 16, 2008

Residence Chiyoda-ku, Tokyo, JAPAN

Citizenship Japanese

Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN

☒ Additional joint inventors or legal representative(s) are named on separately numbered sheets forms PTO/SB/02A or 02LR attached hereto.

Full name of sole or first inventor (given name, family name) Takeshi NAKAYAMA	
Inventor's signature <i>Takeshi Nakayama</i>	Date June 16, 2008
Residence Sanda-shi, Hyogo, JAPAN	Citizenship Japanese
Mailing Address 2-33-8, Hazamagaoka, Sanda-shi, Hyogo 669-1545 JAPAN	
Full name of sole or first inventor (given name, family name) Tomoshi FUTATSUYA	
Inventor's signature <i>Tomoshi Futatsuya</i>	Date June 16, 2008
Residence Chiyoda-ku, Tokyo, JAPAN	Citizenship Japanese
Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN	
Full name of sole or first inventor (given name, family name) Natsuo AJIKA	
Inventor's signature <i>Natsuo Ajika</i>	Date June 16, 2008
Residence Toyonaka-shi, Osaka, JAPAN	Citizenship Japanese
Mailing Address 2-11-34, Okamachiminami, Toyonaka-shi, Osaka 561-0883 JAPAN	
Full name of sole or first inventor (given name, family name) Yuichi KUNORI	
Inventor's signature <i>Yuichi Kunori</i>	Date June 16, 2008
Residence Chiyoda-ku, Tokyo, JAPAN	Citizenship Japanese
Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN	
Full name of sole or first inventor (given name, family name) Hiroshi ONODA	
Inventor's signature <i>Hiroshi Onoda</i>	Date June 16, 2008
Residence Chiyoda-ku, Tokyo, JAPAN	Citizenship Japanese
Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN	
Full name of sole or first inventor (given name, family name) Atsushi FUKUMOTO	
Inventor's signature <i>Atsushi Fukumoto</i>	Date June 16, 2008
Residence Chiyoda-ku, Tokyo, JAPAN	Citizenship Japanese
Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN	

Full name of sole or first inventor (given name, family name) Makoto OHI	
Inventor's signature <i>makoto ohi</i>	Date June 6, 2008
Residence Chiyoda-ku, Tokyo, JAPAN	Citizenship Japanese
Mailing Address c/o Renesas Technology Corp., 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo 100-0004 JAPAN	

~~Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.~~

DECLARATION – Supplemental Priority Data Sheet

Foreign applications:

[illegible]

This collection of information is required by 35 U.S.C. 115 and 37 CFR 1.63. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 21 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.